

ABSTRACT OF THE DISCLOSURE

An evaluator board for testing a printed circuit board assembly device subcomponent includes one or more receiving surfaces mounted on the evaluator board, and a securing mechanism positioned over at least one of the one or more receiving surfaces. The receiving surfaces can include a connection base and an electronic receptacle for receiving conductive elements of the subcomponent to be tested. In one implementation, at least one of the one or more receiving surfaces is configured to receive an edge connector of a form factor pluggable printed circuit board. In addition, one or more active and passive circuitry components can be mounted on opposing surfaces of the evaluator board in order to minimize the size and number of components used in the testing of the PCBA subcomponent.

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